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May 22, 2002

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Assistant Commissioner for Patents
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Attn: APPLICATION DIVISION

Re: U.S. Patent Application Ser. No. 09/980,754
Wilfred Lerch, et al
Group: 2812

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Dear Sir or Madam:

Please find enclosed the Filing Receipt in the above-identified patent application.

It is respectfully requested that a corrected Filing Receipt be issued and sent to the undersigned with the changes as indicated on the enclosed copy.

Your prompt attention to this matter is appreciated.

Very truly yours,


Robert W. Becker

RWB:els
Enclosure



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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/980,754	02/15/2002	2812	1056	Az. 2964	5	22	1

CONFIRMATION NO. 7497

FILING RECEIPT



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Date Mailed: 04/09/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/EP00/03664 04/22/2000

Foreign Applications

GERMANY 199 20 322.9 05/03/1999
GERMANY 199 27 962.4 06/18/1999

Projected Publication Date: Not Applicable, filed prior to November 29, 2000

Non-Publication Request: No

Early Publication Request: No

Title

Method of Generating Defects In a Lattice Structure
of a Semi-Conductor Material
~~Method for generating defects in a grid support of a semiconductor material~~

Preliminary Class

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